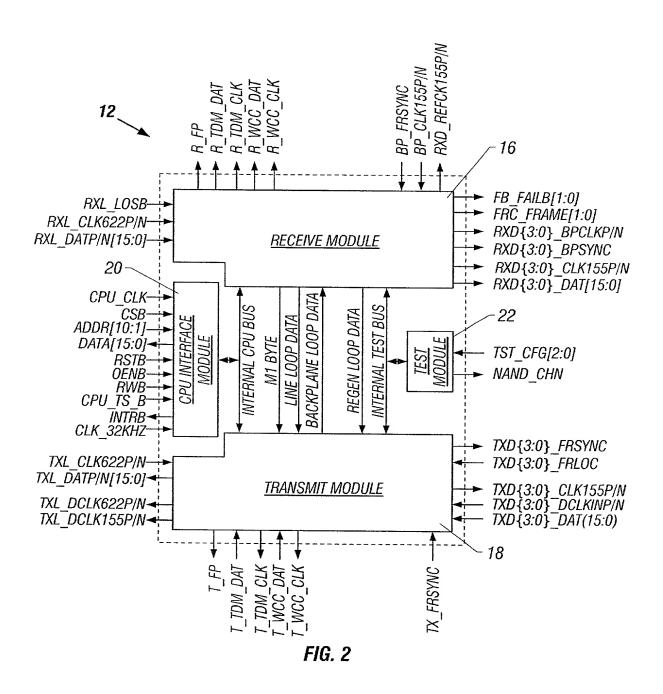
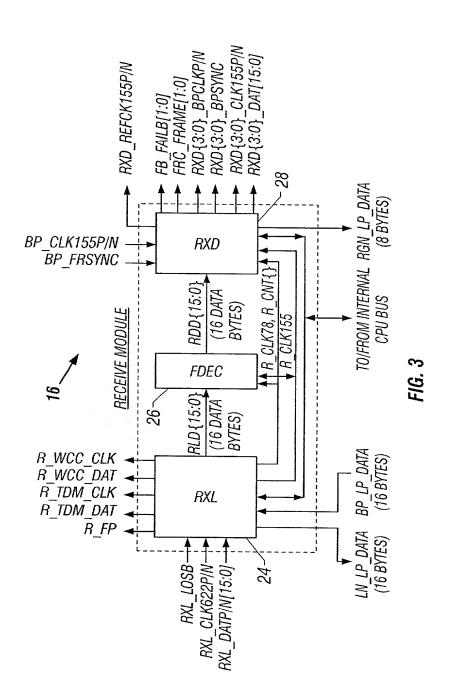


FIG. 1

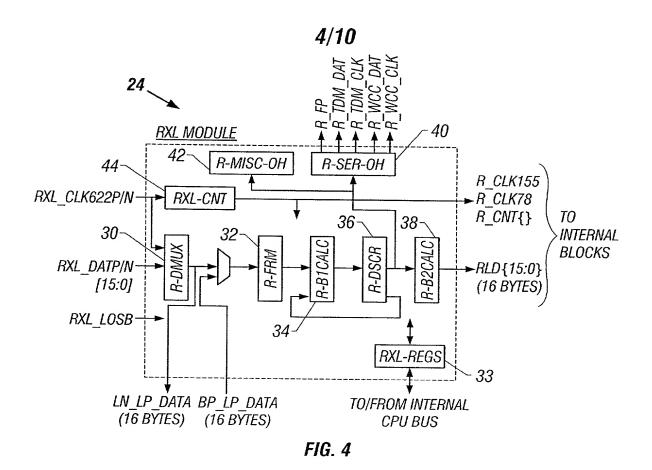
Title: Error Insertion Circuit for SONET Forward Error Correction



Title: Error Insertion Circuit for SONET Forward Error Correction



Title: Error Insertion Circuit for SONET Forward Error Correction



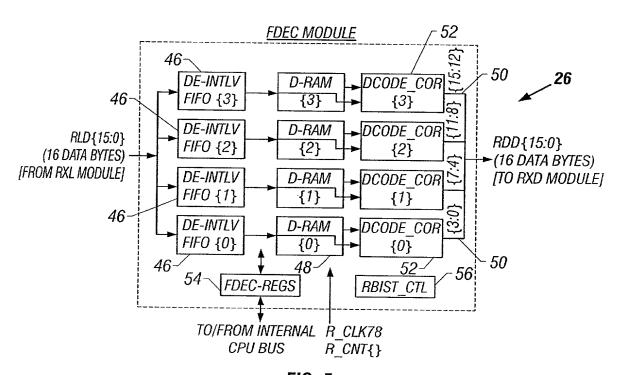


FIG. 5

Title: Error Insertion Circuit for SONET Forward Error Correction

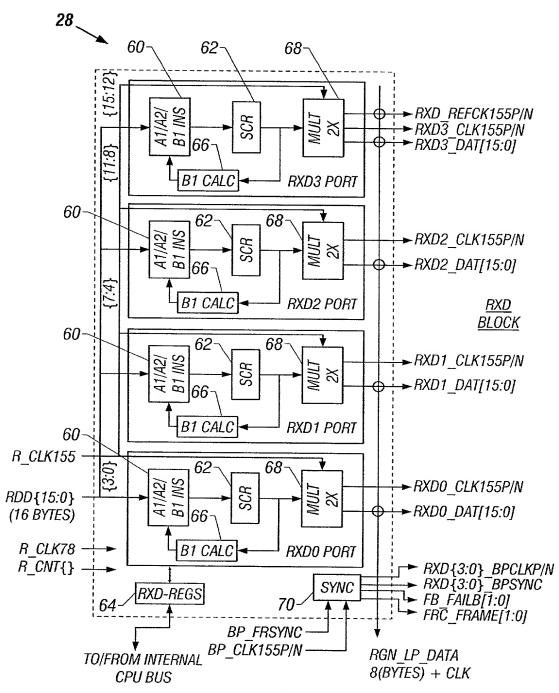
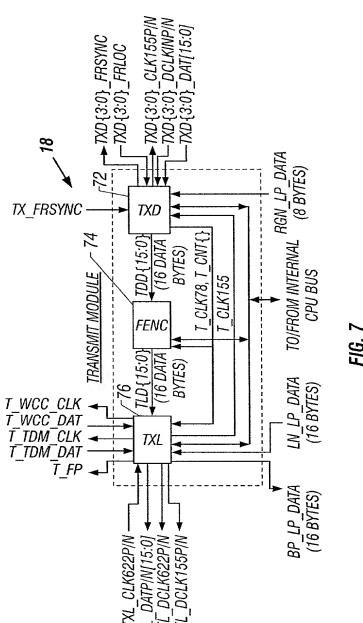
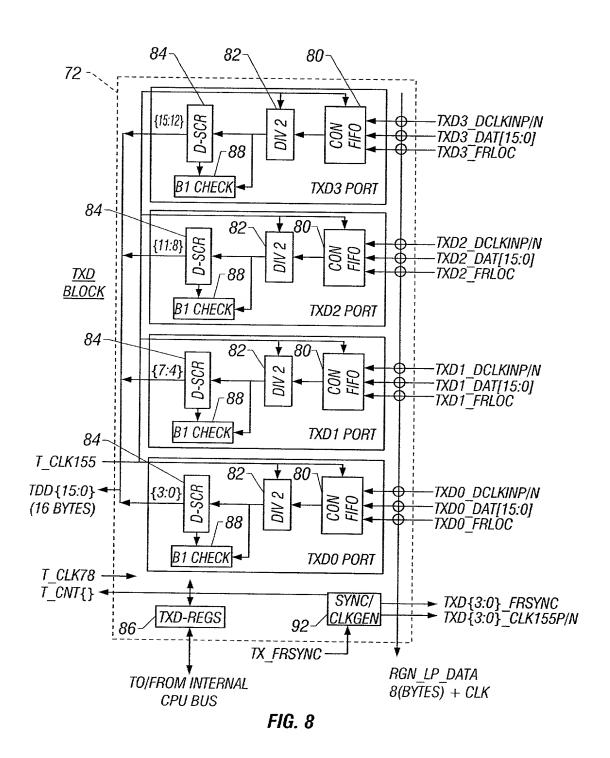


FIG. 6

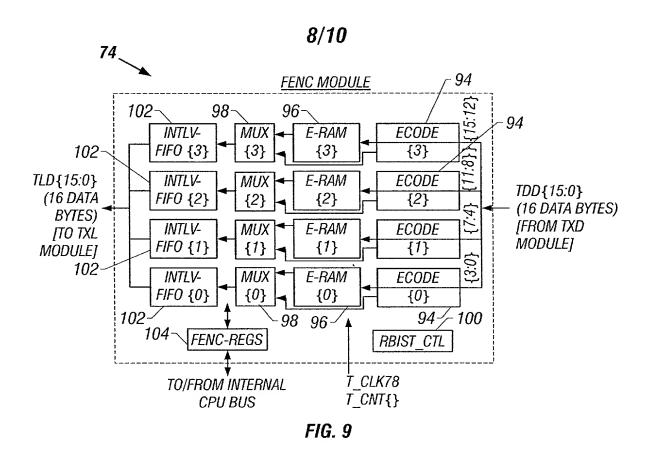
Title: Error Insertion Circuit for SONET Forward Error Correction

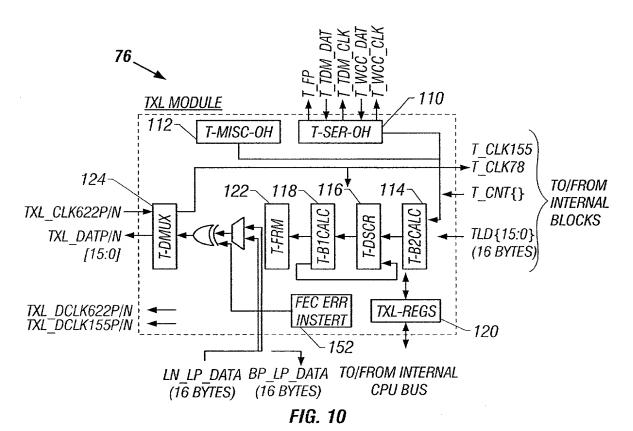


Title: Error Insertion Circuit for SONET Forward Error Correction

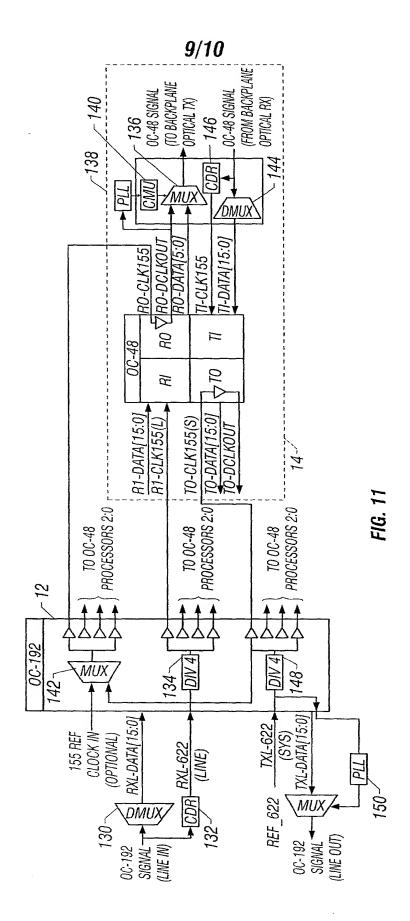


Title: Error Insertion Circuit for SONET Forward Error Correction





Title: Error Insertion Circuit for SONET Forward Error Correction



Title: Error Insertion Circuit for SONET Forward Error Correction

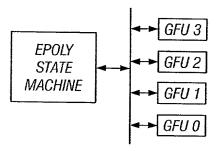


FIG. 12

